

CADENCE KOREA ES SCHEDULE - 1st half of 2020

12/4/2019

Course Name	Instructor	Course ID	Length	S_Date	E_Date	Location	Price
Custom IC Design							
Virtuoso Analog simulation using ADE (G)XL	SCLEE	NA	2	1/16/2020	1/17/2020	IDEC (대전 Kaist)	
SKILL Language Programming	HSHAN/SYLEE	83018	3	2/11/2020	2/13/2020	Cadence Korea	\$378
Virtuoso Layout Design Basics v1C 6.1.8	HSKIM	84460	2	3/4/2020	3/5/2020	Cadence Korea	\$296
Virtuoso Analog Design Environment IC6.1.8	JWCHOI	82083	2	3/19/2020	3/20/2020	Cadence Korea	\$296
Spectre Simulator Fundamentals Spectre 16.1	JSLEE	86210	2	4/2/2020	4/3/2020	Cadence Korea	\$296
Virtuoso Layout Pro(Virtuoso_XL & GXL)	SECHOI/TJLEE	85092	2	4/8/2020	4/9/2020	Cadence Korea	\$296
Mixed Signal Simulation Using Spectre AMS Designer (AVUM)	SWSEO	86135	2	5/14/2020	5/15/2020	Cadence Korea	\$296
SKILL Language Programming	HSHAN/SYLEE	83018	3	5/19/2020	5/21/2020	Cadence Korea	\$378
Virtuoso ADE Explorer & Assembler IC6.1.8	JWCHOI	86253	2	6/11/2020	6/12/2020	Cadence Korea	\$296
Mixed Signal Simulation Using Spectre AMS Designer (AVUM)	SWSEO	NA	2	6/29/2020	6/30/2020	IDEC (대전 Kaist)	
Digital IC Design							
Genus Synthesis Solution with Stylus Common UI	Ahmed	86220	2	1/9/2020	1/10/2020	Cadence Korea	\$296
Innovus Implementation System (Block)	SIPARK	86141	3	2/5/2020	2/7/2020	Cadence Korea	\$444
Tempus Signoff Timing Analysis and Closure	DHHAN	82147	2	2/18/2020	2/19/2020	Cadence Korea	\$296
Innovus Implementation System (Hierarchical)	JSKIM	86142	3	3/11/2020	3/13/2020	Cadence Korea	\$444
Conformal Equivalence Check & Conformal ECO	DRSUH	86133	3	3/30/2020	4/1/2020	Cadence Korea	\$444
Innovus Implementation System (Block)	JYEONG	86141	3	5/27/2020	5/29/2020	Cadence Korea	\$444
Conformal Low Power & IEEE1801	DRSUH	86125	3	6/1/2020	6/3/2020	Cadence Korea	\$444
SystemC Synthesis with Stratus HLS	JCKIM	86170	3	6/15/2020	6/17/2020	Cadence Korea	\$444
Functional Verification							
Xcelium Simulator	IHJUNG	86218	2	2/20/2020	2/21/2020	Cadence Korea	\$296
Xcelium Integrated Coverage	IHJUNG	86225	2	4/23/2020	4/24/2020	Cadence Korea	\$296
Xcelium Simulator	IHJUNG	86218	2	6/18/2020	6/19/2020	Cadence Korea	\$296
Silicon-Package-Board Co-Design							
Allegro PCB Editor Basic Techniques	KHLEE	86097	3	1/14/2020	1/16/2020	Cadence Korea	\$444
Sigrity SystemSI for Parallel Bus Analysis	HJLEE	86216BUS	1	2/14/2020	2/14/2020	Cadence Korea	\$148
Model Generation and Analysis using PowerSI, Broadband SPICE and 3D-EM	HJLEE	86137	1	2/28/2020	2/28/2020	Cadence Korea	\$148
Sigrity PowerDC and OptimizePI	HJLEE	85084	1	3/6/2020	3/6/2020	Cadence Korea	\$148
Allegro PCB Librarian	JOYLEE	86022	1	3/16/2020	3/16/2020	Cadence Korea	\$148
Allegro Design Entry HDL Front-to-Back Flow	JOYLEE	86015	2	3/17/2020	3/18/2020	Cadence Korea	\$296
SiP Layout	KHLEE	86090	3	3/25/2020	3/27/2020	Cadence Korea	\$444
Allegro High-Speed Constraint Management	JOYLEE	86081	2	4/6/2020	4/7/2020	Cadence Korea	\$296
Allegro PCB Editor Basic Techniques	JOYLEE	86097	3	4/20/2020	4/22/2020	Cadence Korea	\$444
Model Generation and Analysis using PowerSI, Broadband SPICE and 3D-EM	DHKANG	86137	1	5/8/2020	5/8/2020	Cadence Korea	\$148
Allegro PCB Editor SKILL Programming Language	MYHAN	86099	3	5/11/2020	5/13/2020	Cadence Korea	\$378
Sigrity SystemSI for Serial Link Analysis	DHKANG	86216LNK	1	5/22/2020	5/22/2020	Cadence Korea	\$148
Allegro AMS Simulator (Pspice)	HJLEE	86087	1	5/26/2020	5/26/2020	Cadence Korea	\$148
Sigrity PowerDC and OptimizePI	MYHAN	85084	1	6/26/2020	6/26/2020	Cadence Korea	\$148

* 교육의 강사는 변경 될 수도 있습니다.

* 상기 일정은 내부사정으로 인해 변경 될 수도 있습니다.

* IDEC 교육은 해당기관 주관으로 진행되는 외부교육입니다.
- 장소: IDEC (대전 Kaist) / 문의: ykim@idec.or.kr

* Course ID를 클릭하시면 과정에 대한 내용 확인 및 등록 가능합니다.
(홈페이지 내 length는 분사기준으로 기재 되어 있습니다. 표에 있는 length를 참조해 주시기 바랍니다.)

1. 교육신청

https://www.cadence.com/content/cadence-www/global/ko_KR/home/training/all-courses.html

2. 교육문의

Email: training_korea@cadence.com / Tel: 031-728-3036

3. 교육요금

교육시작 3일전 (working day 기준)

4. 신청안내

홈페이지에서 등록시 성함은 '국문'으로 기재 바라며, 소속부서까지 기재해야 등록처리 가능합니다.
개인메일로는 신청 불가하며, 회사(혹은 소속 확인이 가능한)메일로만 신청 가능합니다.
(메일로도 신청 가능합니다 - 메일 신청시 회사(소속) 및 부서, 성함, 이메일 주소 기재 필)