



# North America Training Catalog

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Custom Design with Virtuoso Technology (CIC)					
Product #	Course Title	Instructional Days	Classroom Price	Online	Digital Badge
Prices and Number of Days subject to change					
84401	Advanced SKILL Language Programming	3	\$ 2,220	•	•
85086	Analog Modeling and Simulation with SPICE	4	\$ 2,960		
82086	Analog Modeling with Verilog-A	3	\$ 2,220	•	•
84455	Behavioral Modeling with Verilog-AMS	2	\$ 1,480	•	•
86244	Command-Line Based Mixed-Signal Simulations with the Xcelium Use Model	2	\$ 740	•	•
86234	Design Checks and Asserts	1	\$ 740	•	
86241	Microwave Office for RF Designers	3	\$ 2,220	•	•
86241	Mixed Signal Simulations Using Spectre AMS Designer	2	\$ 1,480	•	•
86287	High-Performance Spectre Simulation	1	\$ 740	•	•
86267	Pegasus Verification System	2	\$ 1,480	•	•
85079	Physical Verification Language Rules Writer	2	\$ 1,480		
84509	Physical Verification System	2	\$ 1,480	•	•
86148	Quantus Transistor-Level T1: Overview and Technology Setup	0.5	\$ 370	•	•
86149	Quantus Transistor-Level T2: Parasitic Extraction	1	\$ 740	•	•
86150	Quantus Transistor-Level T3: Extracted View Flows and Advanced Features	0.5	\$ 370	•	•
85058	Real Modeling with SystemVerilog	3	\$ 2,220	•	
85059	Real Modeling with Verilog-AMS	2	\$ 1,480	•	
84454	Simulation and Analysis Using OCEAN	2	\$ 1,480		
86301	SimVision for Debugging Mixed-Signal Simulations	1	\$ 740		
84422	SKILL Development of Parameterized Cells	2	\$ 1,480	•	•
83018	SKILL Language Programming	5	\$ 3,700	•	•
85099	SKILL Language Programming Fundamentals	3	\$ 2,220		
84508	SKILL Language Programming Introduction	2	\$ 1,480	•	•
86203	Spectre RF Analysis using Harmonic Balance	2	\$ 1,480	•	
86204	Spectre RF Analysis using Shooting Newton Method	2	\$ 1,480		
86171	Spectre Simulator Fundamentals S1: Spectre Basics	1	\$ 740	•	•
86172	Spectre Simulator Fundamentals S2: Large-Signal Analyses	1	\$ 740	•	•
86173	Spectre Simulator Fundamentals S3: Small-Signal Analyses	1	\$ 740	•	•
86174	Spectre Simulator Fundamentals S4: Measurement Description Language	0.5	\$ 370	•	•
86197	Virtuoso Abstract Generator	1	\$ 740	•	
86253	Virtuoso ADE Explorer and Assembler S1: ADE Explorer and Single Test Corner Analysis	0.5	\$ 370	•	•
86254	Virtuoso ADE Explorer and Assembler S2: ADE Assembler and Multi Test Corner Analysis	1	\$ 740	•	•
86255	Virtuoso ADE Explorer and Assembler S3: Sweeping Variables and Simulating Corners	0.5	\$ 370	•	•
86256	Virtuoso ADE Explorer and Assembler S4: Monte Carlo Analysis, Real-Time Tuning and Run Plans	0.5	\$ 370	•	•
86183	Virtuoso ADE Verifier S1: Setup, Run and View Verification Results	1	\$ 740	•	•
86250	Virtuoso ADE Verifier S2: Reference Flow and Analog Coverage Using the Setup Library Assistant	1	\$ 740	•	•
85081	Virtuoso Connectivity-Driven Layout Transition	2	\$ 1,480	•	•
84466	Virtuoso Floorplanner	1	\$ 740	•	

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## Custom Design with Virtuoso Technology (CIC) continued from page 1

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84460	Virtuoso Layout Design Basics	1	\$ 740	•	•
85066	Virtuoso Layout for Advanced Nodes	2	\$ 1,480	•	
86270	Virtuoso Layout for Advanced Nodes and Methodology Platform	0.5	\$ 370	•	
86212	Virtuoso Layout for Advanced Nodes: T1 Place and Route	1	\$ 740	•	•
86221	Virtuoso Layout for Advanced Nodes: T2 Electromigration	0.5	\$ 370	•	•
85087	Virtuoso Layout Pro: T1 Environment and Basic Commands (L)	1	\$ 740	•	•
85088	Virtuoso Layout Pro: T2 Create and Edit Commands (L)	1	\$ 740	•	•
85089	Virtuoso Layout Pro: T3 Basic Commands (XL)	1	\$ 740	•	•
85090	Virtuoso Layout Pro: T4 Advanced Commands (XL)	1	\$ 740	•	•
85091	Virtuoso Layout Pro: T5 Interactive Routing (XL)	0.5	\$ 370	•	•
86120	Virtuoso Layout Pro: T6 Constraint-Driven Flow and Power Routing	0.5	\$ 370	•	•
86121	Virtuoso Layout Pro: T7 Module Generator and Floorplanner (XL/GXL)	0.5	\$ 370	•	•
86121	Virtuoso Layout Pro: T8 Virtuoso Concurrent Layout Editing	1	\$ 740	•	
86265	Virtuoso Layout Pro: T9 Virtuoso Design Planner	2	\$ 1,480	•	•
84443	Virtuoso Schematic Editor	2	\$ 1,480	•	•
86283	Virtuoso Simulation Driven Routing (SDR)	0.5	Online only	•	
86139	Virtuoso Spectre Pro: S1 DC Algorithm	0.5	\$ 370	•	•
86159	Virtuoso Spectre Pro S2: AC, XF, STB, and Noise Analyses	0.5	\$ 370		
86160	Virtuoso Spectre Pro S2: Transient Algorithm	0.5	\$ 370		
86161	Virtuoso Spectre Pro S4: Measuring Accurate Fourier Transforms	0.5	\$ 370		
86162	Virtuoso Spectre Pro S5: Transient Noise	1	\$ 740		
86245	Virtuoso System Design Platform	1	\$ 740	•	
85040	Virtuoso Visualization and Analysis	1	\$ 740	•	•
86272	5G mmWave Handset System Design – S1: RFIC (Transceiver) Design	1	\$ 740	•	•

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## System Design and Verification (FV)

Product #	Course Title	Instructional Days	Classroom Price	Online	Digital Badge
<small>Prices and Number of Days subject to change</small>					
82119	C++ Language Fundamentals	2	\$ 1,260	•	•
86140	Essential SystemVerilog for UVM	1	\$ 630	•	
84506	Foundations of Metric Driven Verification	1	\$ 740	•	
86152	Inclusive Functional Safety Simulator	1	\$ 740		
82115	Inclusive SystemC, VHDL, and Verilog Simulation	1	\$ 740		
86123	JasperGold Formal Fundamentals	2.5	\$ 1,850	•	
86268	JasperGold Formal Expert	2	\$ 1,480	•	•
82149	Low-Power Simulation with CPF	2	\$ 1,480	•	
85080	Low-Power Simulation with IEEE Std 1801™ UPF	2	\$ 1,480	•	•
86132	Metric Driven Verification using Cadence vManager	3	\$ 2,220	•	•
86043	PCB Design at RF - multi-Gigabit Transmission, EMI Control, and PCB Materials	2	\$ 1,480		
86213	Perspec System Verifier – Basic	5	Online only	•	•
82133	Specman Advanced Verification	4	\$ 2,960		
85034	Specman Fundamentals for Block-Level Environment Developers	5	\$ 3,700	•	•
86235	SVA, Formal and Jaspergold Fundamentals for Designers	3	Online only	•	•
82202	SystemC Language Fundamentals	3	\$ 1,890	•	•
86170	SystemC Synthesis with Stratus HLS	3	\$ 1,890		
84488	SystemC Transaction-Level Modeling (TLM 2.0)	2	\$ 1,260	•	
86070	SystemVerilog Accelerated Verification with UVM	4	\$ 2,520	•	•
85045	SystemVerilog Advanced Register Verification Using UVM	2	\$ 1,260		
82165	SystemVerilog Assertions	2	\$ 1,260	•	•
82143	SystemVerilog for Design and Verification	5	\$ 3,150	•	•
82159	SystemVerilog Language for Verification	3	\$ 1,890		
86274	SystemVerilog Real Number Modeling (SV-RNM) Based Advanced Verification	5	\$ 3,150		
82158	Tcl Scripting for EDA + Intro to Tk	3	\$ 2,220		•
82110	Verilog Language and Application	4	\$ 2,520	•	•
86238	VIP Basic Building Blocks and Usage	2	\$ 1,480	•	
82109	VHDL Language and Application	5	\$ 3,150	•	
86178	vManager Tool Usage in Batch Mode	0.5	\$ 370		
86225	Xcelium Integrated Coverage	2	\$ 1,480	•	•
86218	Xcelium Simulator	2	\$ 1,480	•	•

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Digital Design and Signoff (ICD)					
Product #	Course Title	Instructional Days	Classroom Price	Online	Digital Badge
<small>Prices and Number of Days subject to change</small>					
86249	Advanced Synthesis with Genus Stylus Common UI	2	\$ 1,480	•	•
84452	Basic Static Timing Analysis	2	\$ 1,480	•	•
86136	Cadence RTL-to-GDSII Flow	2	\$ 1,480	•	•
86136	Cadence Cerebrus Intelligent Chip Explorer	1	\$ 740	•	
86269	Clarity 3D Solver	2	\$ 1,480	•	•
82194	Conformal ECO	1	\$ 740	•	•
82123	Conformal Equivalence Checking	2	\$ 1,480	•	•
82156	Conformal Low-Power Verification	1	\$ 740	•	•
86155	Fundamentals of IEEE 1801 Low-Power Specification Format	1	\$ 740	•	•
86164	Genus Low-Power Synthesis Flow with IEEE 1801	1	\$ 740	•	•
86164	Genus Synthesis Solution	2	\$ 1,480	•	
86220	Genus Synthesis Solution with Stylus Common UI	3	\$ 1,480	•	•
86222	Innovus Block Implementation with Stylus Common UI	3	\$ 2,220	•	•
86198	Innovus Clock Concurrent Optimization Technology for Clock Tree Synthesis	1	\$ 740	•	
86230	Innovus Clock Concurrent Optimization Technology with Stylus Common UI	1	\$ 740	•	
86229	Innovus Hierarchical Implementation with Stylus Common UI	1	\$ 740	•	•
86141	Innovus Implementation System (Block)	3	\$ 2,220	•	•
86142	Innovus Implementation System (Hierarchical)	1	\$ 740	•	•
86259	Innovus Low-Power Flow with Stylus Common UI	1	\$ 740	•	
86189	Joules Power Calculator	2	\$ 1,480	•	•
86143	Low-Power Flow with Innovus Implementation System (Libraries not included for Online)	1	\$ 740	•	
86243	Low-Power Synthesis Flow with Genus Stylus Common UI (Libraries not included for Online)	1	\$ 740	•	•
82137	Modus DFT Software Solution	1	\$ 740		
82147	Tempus Signoff Timing Analysis and Closure	2	\$ 1,480	•	•
86226	Tempus Signoff Timing Analysis and Closure with Stylus Common UI	2	\$ 1,480	•	•
86239	Test Synthesis with Genus Stylus Common UI	2	\$ 1,480	•	•
82141	Virtuoso Digital Implementation	3	\$ 2,220	•	
82169	Voltus Power-Grid Analysis and Signoff	2	\$ 1,480	•	•
86232	Voltus Power Grid Analysis and Signoff with Stylus Common UI	2	\$ 1,480	•	•

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Tensilica Design IP (IP)					
Product #	Course Title	Instructional Days	Classroom Price	Online	Digital Badge
<small>Prices and Number of Days subject to change</small>					
86049	Tensilica Audio Codec API	0.5	Online only	•	
86304	Tensilica ConnX 110 and 120 DSP Family	2	\$ 2,400		
86258	Tensilica ConnX B10 DSP	2	\$ 2,400	•	
86260	Tensilica ConnX B20 DSP	2	\$ 2,400		
86194	Tensilica ConnX BBE16EP Baseband Engine	2	\$ 2,400		
86062	Tensilica ConnX BBE32EP Baseband Engine	2	Online only	•	
86050	Tensilica ConnX BBE64EP Baseband Engine	2	\$ 2,400		
86266	Tensilica DNA 100 Architecture and Programming	1	Online only	•	
86288	Tensilica Vision DSP Family	2	Online only	•	
86154	Tensilica Fusion F1 DSP	1	Online only	•	
86200	Tensilica Fusion G3 DSP	2	Online only	•	
86214	Tensilica Fusion G6 DSP	2	\$ 2,400		
86280	Tensilica FloatingPoint DSP Family	2	Online only	•	
86057	Tensilica HiFi 2/EP/Mini Audio Engine ISA	1	Online only	•	
86056	Tensilica HiFi 3 Audio Engine ISA	0.7	Online only	•	
86227	Tensilica HiFi 4 DSP	1	Online only	•	
86262	Tensilica HiFi 5 DSP	1	Online only	•	
86039	Tensilica Instruction Extension Language and Design	1	\$ 1,200	•	
86037	Tensilica Xtensa LX Processor Fundamentals	2	Online only	•	•
86251	Tensilica System Modeling using XTSC	1	Online only	•	
86201	Tensilica Vision P6 DSP	2	Online only	•	
86065	Tensilica Xtensa LX Hardware Verification and EDA	0.5	\$ 600	•	
86261	Tensilica Vision Q7 DSP	1	Online only	•	
86276	Tensilica Xtensa Audio Framework	0.5	Online only	•	
86277	Tensilica Xtensa Neural Network Compiler v2	1.5	Online only	•	
86237	Tensilica Xtensa NX Hardware Verification and EDA	1	Online only	•	
86066	Tensilica Xtensa LX Processor Interfaces	0.5	Online only	•	
86257	Tensilica Xtensa NX Processor Fundamentals	2	Online only	•	•
86236	Tensilica Xtensa NX Processor Interfaces	1	Online only	•	

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PCB and Package Design with Allegro Technology (SPB)				
Product #	Course Title	Instructional Days	Classroom Price	Online Digital Badge
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86217	Advanced Design Verification with the RAVEL Programming Language	2	\$ 1,480	•
85093	Advanced PSpice for Power Users	2	\$ 1,020	
86087	Allegro AMS Simulator	3	\$ 2,220	•
86095	Allegro AMS Simulator Advanced Analysis	1	\$ 740	•
85053	Allegro Design Entry HDL Basics	1	\$ 740	•
86015	Allegro Design Entry HDL Front-to-Back Flow	3	\$ 2,220	• •
86100	Allegro Design Entry HDL SKILL Programming Language	3	\$ 2,220	•
85037	Allegro Design Entry Using OrCAD Capture	2	\$ 1,480	•
86083	Allegro Design Reuse	1	\$ 740	•
86088	Allegro Design Workbench for Librarians	2	\$ 1,480	
86240	Allegro EDM Design Entry HDL Front-to-Back Flow	3	\$ 2,220	•
86242	Allegro EDM PCB Librarian	2	\$ 1,480	•
84479	Allegro FPGA System Planner	2	\$ 1,480	•
86081	Allegro High-Speed Constraint Management	2	\$ 1,480	• •
85022	Allegro Package Designer	4	\$ 2,960	•
86263	Allegro Package Designer Plus	3	\$ 2,220	•
86205	Allegro PCB Editor Advanced Methodologies	1	\$ 740	• •
86097	Allegro PCB Editor Basic Techniques	3.5	\$ 2,590	• •
86098	Allegro PCB Editor Intermediate Techniques	2	\$ 1,480	• •
86099	Allegro PCB Editor SKILL Programming Language	3	\$ 2,220	• •
86022	Allegro PCB Librarian	2	\$ 1,480	• •
86019	Allegro PCB Router Basics	2	\$ 1,480	• •
86105	Allegro RF PCB	1	\$ 740	•
85072	Allegro Sigrity Package Assessment and Model Extraction	1	\$ 740	•
86060	Allegro Sigrity PI	1	\$ 740	•
85073	Allegro Sigrity SI Foundations	2	\$ 1,480	•
86094	Allegro System Architect	2	\$ 1,480	•
86228	Allegro System Capture	2.5	\$ 1,850	•
85069	Allegro Team Design Authoring	1	\$ 740	•
85095	Allegro Update Training	2	\$ 1,480	•
85051	Analog Simulation with PSpice	3	\$ 1,530	•
85054	Analog Simulation with PSpice Advanced Analysis	1	\$ 510	•
85051	Analog Simulation with PSpice using System Capture	3	\$ 2,220	• •
86273	Celsius Thermal Solver	1	\$ 740	• •
86042	Essential High-speed PCB Design for Signal Integrity	3	\$ 2,220	
86137	Model Generation and Analysis using PowerSI, Broadband SPICE	3	\$ 2,220	• •
86169	OrbitIO System Planner	1	\$ 740	•
86271	OrCAD Capture Constraint Manager PCB Flow	1	\$ 510	•
86271	OrCAD CIS	1	\$ 510	
86281	Sigrity Aurora	3	\$ 2,220	• •
85084	Sigrity PowerDC and OptimizePI	1	\$ 740	• •
86216	Sigrity SystemSI for Parallel Bus and Serial Link Analysis	3	\$ 2,220	•
86090	SIP Layout	4	\$ 3,700	• •
86278	TopXplorer SystemSI for Parallel Bus and Serial Link Analysis	2	\$ 1,480	•

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