Cadence Training Services learning maps provide a comprehensive visual overview of the learning opportunities for Cadence customers. They provide recommended course flows as well as tool experience and knowledge levels to guide students through a complete learning plan. Learning Maps cover all Cadence® technologies and reference courses available worldwide. For course names, descriptions, and schedules, please select the Browse Catalog button at https://www.cadence.com/training.

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Custom IC, Analog and RF Design Learning Map

Circuit Design, Simulation, Modeling and RF Design

Analog Modeling with Verilog-A
- Mixed-Signal Simulations using Spectre AMS Designer
- Command-Line Based Mixed-Signal Simulations w/ Xcelium Use Model
- Behavioral Modeling with Verilog AMS
- Behavioral Modeling with VHDL-AMS
- Real Modeling with Verilog-AMS
- Real Modeling with SystemVerilog
- SystemVerilog Real Number Modeling (SV-RNM) Based Advanced Verification

Virtuoso Schematic Editor
- Virtuoso Visualization and Analysis
- Virtuoso® ADE Explorer & Assembler Series
  - S1 ADE Explorer & Single Test Corner Analysis
  - S2 ADE Assembler & Multi Test Corner Analysis
  - S3 Sweeping Variables and Simulating Corners
  - S4 Monte Carlo, Real-Time Tuning & Run Plans

Virtuoso® ADE Verifier Series
- S1 Setup, Run, & View Verifier Results
- S2 Reference Flow and Analog Coverage Using the Setup Library Assistant

Design Checks and Asserts

Virtuoso® Spectre® Pro Series
- S1 DC Algorithm
- S2 AC, XF, STB, Noise
- S3 Transient Algorithm
- S4 Fourier Transform
- S5 Transient Noise

Spectre® RF Shooting Newton

Spectre XPS for Mixed-Signal Designs

Virtuoso Visualization and Analysis

Spectre® Simpler® Fundamentals Series
- S1 Spectre Basics
- S2 Large-Signal
- S3 Small-Signal
- S4 Spectre MDL

Virtuoso Schematic Editor

5G mmWave Handset System Design – S1 RFIC (Transceiver) Design

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