Cadence Training Learning Maps

Cadence Training Services learning maps provide a comprehensive visual overview of the learning opportunities for Cadence customers. They provide recommended course flows as well as tool experience and knowledge levels to guide students through a complete learning plan. Learning Maps cover all Cadence® Technologies and reference courses available worldwide. For course names, descriptions, and schedules, please select the Browse Catalog button at https://www.cadence.com/training.

Contents
• PCB Design and Analysis
• Custom IC, Analog, and RF Design
• Digital Design and Signoff
• System Design and Analysis
• IC Package Design and Analysis
• Tensilica® Processor IP
### IC Package Design

<table>
<thead>
<tr>
<th>Course</th>
<th>Tiers</th>
<th>Days</th>
</tr>
</thead>
<tbody>
<tr>
<td>SiP Layout</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Allegro® Package Designer</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>Allegro FPGA System Planner</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>Allegro Sigrity Package Assessment and Model Extraction</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>OrbitIO™ System Planner</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>Advanced Design Verification with the RAVEL Programming Language</td>
<td>2</td>
<td></td>
</tr>
</tbody>
</table>

### SI/PI Analysis

<table>
<thead>
<tr>
<th>Course</th>
<th>Tiers</th>
<th>Days</th>
</tr>
</thead>
<tbody>
<tr>
<td>Allegro Sigrity™ SI Foundations</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>Allegro Sigrity PI</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>Sigtry PowerDC™ and OptimizePI™</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>Sigtry SystemSI™ for Parallel Bus and Serial Link Analysis</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>Model Generation and Analysis using PowerSI, Broadband SPICE, and 3D-EM</td>
<td>2</td>
<td></td>
</tr>
</tbody>
</table>