Learning Maps

Cadence Training Services learning maps provide a comprehensive visual overview of the learning opportunities for Cadence customers. They provide recommended course flows as well as tool experience and knowledge levels to guide students through a complete learning plan. Learning Maps cover all Cadence® technologies and reference courses available worldwide. For course names, descriptions, and schedules, please select the Browse Catalog button at

https://www.cadence.com/training

Contents

- PCB Design and Analysis
- Custom IC, Analog, and RF Design
- Digital Design and Signoff
- System Design and Analysis
- IC Package Design and Analysis
- Tensilica® Processor IP
- Computational Fluid Dynamics
<table>
<thead>
<tr>
<th>IC Package Design</th>
<th>SI/PI Analysis</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>SiP Layout</strong></td>
<td><strong>Allegro Sigrity™ SI Foundations</strong></td>
</tr>
<tr>
<td><strong>Allegro® Package Designer</strong></td>
<td><strong>Allegro Sigrity PI</strong></td>
</tr>
<tr>
<td><strong>Allegro FPGA System Planner</strong></td>
<td><strong>Sigrit PowerDC™ and OptimizePI™</strong></td>
</tr>
<tr>
<td><strong>Allegro Sigrity Package Assessment and Model Extraction</strong></td>
<td><strong>TopXplorer SystemSI for Parallel Bus and Serial Link Analysis</strong></td>
</tr>
<tr>
<td><strong>OrbitIO™ System Planner</strong></td>
<td><strong>Model Generation and Analysis using PowerSI, Broadband SPICE, and 3D-EM</strong></td>
</tr>
<tr>
<td><strong>Advanced Design Verification with the RAVEL Programming Language</strong></td>
<td><strong>Clarity 3D Solver</strong>&lt;br&gt;NEW&lt;br&gt;3&lt;sup&gt;rd&lt;/sup&gt;</td>
</tr>
<tr>
<td><strong>Allegro Package Designer Plus</strong>&lt;br&gt;NEW&lt;br&gt;3&lt;sup&gt;rd&lt;/sup&gt;</td>
<td><strong>Celsius Thermal Solver</strong>&lt;br&gt;NEW&lt;br&gt;1&lt;sup&gt;st&lt;/sup&gt;</td>
</tr>
</tbody>
</table>

New Course<br>Number of days for instructor-led course<br>Tiers of Cadence products used in course<br>Online Course Available

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Circuit Modeling, Analog/Mixed-Signal/RF Circuit Design and Simulation

### Mixed-Signal Simulation
- Mixed-Signal Simulations using Spectre AMS Designer
- Command-Line Based Mixed-Signal Simulations w/ Xcelium Use Model
- SimVision for Debugging Mixed-Signal Simulations

### AMS/Real Number Modeling
- Analog Modeling with Verilog-A
- Behavioral Modeling with Verilog-AMS
- Real Modeling with SystemVerilog
- SystemVerilog Real Number Modeling (SV-RNM) Based Advanced Verification

### Virtuoso® ADE Explorer & Assembler Series
- S1 ADE Explorer & Single Test Corner Analysis
- S2 ADE Assembler & Multi Test Corner Analysis
- S3 Sweeping Variables and Simulating Corners
- S4 Monte Carlo, Real-Time Tuning & Run Plans

### Virtuoso® ADE Verifier Series
- S1 Setup, Run, & View Verifier Results
- S2 Reference Flow and Analog Coverage Using the Setup Library Assistant

### 5G mmWave Handset System Design – S1 RFIC (Transceiver) Design

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### Spectre® Simulator Fundamentals Series
- S1 Spectre Basics
- S2 Large-Signal Analysis
- S3 Small-Signal Analysis
- S4 Spectre MDL

### High Performance Spectre Simulation
- Virtuoso® Spectre® Pro Series
  - S1 DC Algorithm
  - S2 AC, XF, STB, Noise
  - S3 Transient Algorithm
  - S4 Fourier Transform
  - S5 Transient Noise

### Spectre RF Series
- Spectre® RF Shooting Newton
- Spectre® RF Harmonic Balance

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New Course  
Number of days for instructor-led course  
Tiers of Cadence products used in course  
Online Course Available  
Digital Badge Available  
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System Design and Verification Learning Map

Simulation, Coverage and Debug

Xcelium™ Simulator

Cadence® RTL-To-GDSII Flow

VIP Basic Building Blocks and Usage

Low-Power Simulation with CPF

Low-Power Simulation with IEEE1801 UPF

Foundations of Metric-Driven Verification

Xcelium Integrated Coverage

Metric-Driven Verification Using vManager™

vManager Tool Usage in Batch Mode

Perspec™ System Verifier - Basic

Xcelium Fault Simulator

Specman® Fundamentals for Block-Level Environment Developers

Digital Badge Available

New Course

Number of days for instructor-led course

Tiers of Cadence products used in course

Online Course Available

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<table>
<thead>
<tr>
<th>Level</th>
<th>Course</th>
<th>Duration (Days)</th>
<th>Tiers of Cadence Products</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Advanced</strong></td>
<td>Fidelity Turbo: Introduction</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Fidelity Automesh for Unstructured Meshing</td>
<td>1.5</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Fidelity Flow</td>
<td>1.5</td>
<td></td>
</tr>
<tr>
<td><strong>Beginner</strong></td>
<td>Fine Marine for Beginners</td>
<td>2.5</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Fine Marine for Advanced Users</td>
<td>1.75</td>
<td></td>
</tr>
</tbody>
</table>