The VirtualBridge adapter for PCI Express 2.0/3.0 consists of a PCIe protocol transactor that enables high-speed transfers between a user’s design under test (DUT) running in a Palladium platform and a host workstation. A user’s application can drive traffic via their existing OS driver into the transactor, either directly on the same host workstation (see Figure 1) or via a networked connection.

The VirtualBridge adapter for PCIe 2.0/3.0 is a fully static solution, enabling full clock control. This allows clocks to be stopped without encountering motherboard/chipset timeout issues. Users can, for example, pause an active emulation to upload metrics to a workstation to analyze design performance.

**BENEFITS**
- Remotely access designs in any Palladium hardware domain
- Remotely connect from any workstation
- Enables more engineers to run designs with 24/7 access
- Fully static solution

**FEATURES**
- Software adapter for user application and OS drivers to establish a virtual PCIe connection to Palladium platforms
- Fully static PCIe operations
- Multiple OS platform support via VM
VirtualBridge Adapter for PCI Express 2.0/3.0

- Flexible network-enabled architecture
- Multi-function, multi-VM, multi-device, multi-bar
- DMA operations
- Upstream-downstream parallelism
- 64-bit BAR support
- Installer and Setup Wizard GUI
- IXCOM console
- Debug and profiler CLI
- Logging and user control
- Downstream memory and DMA checker
- Socket performance analyzer
- OS: X86 architecture: Linux, Windows

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