

Memory and I/O Adapter for Palladium Z1 Platform

Full system-verification solution

The Cadence® Memory and I/O adapter provides a simple and efficient interface that enables emulation teams to directly integrate industry-standard commercial software debuggers into the Cadence Palladium® Z1 Enterprise Emulation Platform. The Memory and I/O Adapter for Palladium Z1 Platform provides multiple JTAG interface connectors for Cadence, Arm®, TI, ADI, Freescale, and NXP processors. Additionally, multiple UART interfaces are also provided for software debugging in a multi-core SoC environment. Along with these primary software debugger interfaces, other peripheral interfaces associated with many SoC-based designs, such as SD card and video, are also provided.

Hardware Features

- ▶ Connects directly to the Palladium Z1 TPOD through the HDDC connector
- ▶ Supports JTAG, UART, and SD card interface
- ▶ Supports most of the current industry-leading JTAG debuggers
- ▶ Provides convenient UART to USB interface to enable easy access with standard laptops
- ▶ Provides board-to-board connections for support of Video SpeedBridge® Adapters
- ▶ Provides breadboard array to enable customers to easily add custom headers
- ▶ Provides probe headers for use with external logic analyzers to assist with system debug
- ▶ For low-power applications, board can be powered directly from the Palladium Z1 TPOD
- ▶ Provides optional external PSU with the kit for higher power applications



Specifications

The Memory and I/O Adapter for Palladium Z1 Platform supports:

- ▶ One Cadence Tensilica® 14-pin JTAG interface
- ▶ Two UART DB9 interfaces
- ▶ Four UART interfaces through USB interface
- ▶ Two Arm 20-pin JTAG interfaces
- ▶ Two each of 10-pin and 20-pin Arm CoreSight™ interfaces
- ▶ One Arm Mictor 38-pin TRACE header
- ▶ One TI 14-pin JTAG interface
- ▶ One ADI 14-pin JTAG interface
- ▶ One Infineon 16-pin and one 10-pin JTAG interface
- ▶ One NXP 16-pin JTAG interface
- ▶ Provides breadboard area of 32x16 array using 0.1" spaced through-holes
- ▶ Provides two additional headers for board-to-board connections
 - Provided with the kit is a Video SpeedBridge Adapter supporting two video SpeedBridge connections
- ▶ Two SD card slots
- ▶ All TPOD I/O accessible on probe headers for use with external logic analyzers and oscilloscopes

Requirements

- ▶ Requires one TPOD
- ▶ Miscellaneous RF models, including the DC block, DC feedthrough, and microstrip and stripline elements (bend, cross, corner, curve, open line, tee models)

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