

High-Density SpeedBridge Adapter for Ethernet

Rapid system-level verification deployment

Cadence® SpeedBridge® Adapters provide protocol interface solutions for an emulation platform that enables efficient driver and application-level testing targeting validation of pre-silicon RTL of ASICs and systems on chip (SoCs) with a Cadence Palladium® emulation system or Cadence Protium™ prototyping system. SpeedBridge Adapters can also be leveraged to quickly and easily reproduce post-silicon bugs. The solution enables verification of emulated designs along with development, testing, and performance characterization of embedded software, system-level drivers, and final software applications.

High-Density SpeedBridge Adapter for Ethernet

Ethernet has become the most commonly used LAN technology worldwide. To address the verification and integration needs of Ethernet network devices, Cadence offers an off-the-shelf emulation solution for Ethernet designs. The High-Density SpeedBridge Adapter for Ethernet offers fast bring-up times, a fast path to sophisticated verification platform, an easy connection to live networks or testers, and the ability to scale to many ports without impacting performance.

The High-Density SpeedBridge Adapter for Ethernet connects emulated Ethernet adapter designs to networks or Ethernet testers at full speed through RJ45 connectors. It does not perform any switching or routing of packets—it simply forwards packets on the network side to the emulation side, and vice versa. The board operates as a transparent rate adapter between full-speed network traffic and Cadence emulation systems.

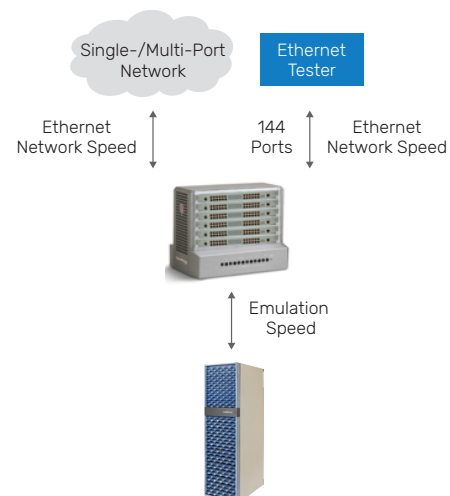


Figure 1: The High-Density SpeedBridge Adapter for Ethernet enables high-performance emulation system verification using a single-port or multi-port Ethernet tester.

When connected to a Palladium Z1 series verification platform, the High-Density SpeedBridge Adapter for Ethernet can be used in a variety of ways to verify your design. For example, you can verify your design mapped into a Palladium Z1 platform by connecting through the High-Density SpeedBridge Adapter for Ethernet to a live network (see Figure 1).

With the high speed of emulation, you can co-verify hardware and software running on the design with the application software running over an Ethernet network. You can have both hardware and software debug tools for ease of use, ease of debug, and high speed—so you don't have to sacrifice quality.

You can verify your design mapped into a Palladium Z1 series system with any standard Ethernet tester that can support PAUSE flow control (see Figure 1). The tester provides complex stimulus to verify your design.

One High-Density SpeedBridge Adapter for Ethernet can support up to 24 ports of GMII, XGMII, XLGMII, CGMII, 200G GMII, or 400G GMII.

Max SpeedBridge Adapter Performance*		
GMII	12p/s	2.4Mb/s
XGMII	95p/s	49Mb/s
XLGMII (128 bits)	190Kp/s	128Mb/s
CGMII (192 bits)	286Kp/s	192Mb/s
200G GMII (256 bits)	380Kp/s	256Mb/s
400G GMII (512 bits)	762Kp/s	512Mb/s
CGMII type inter- faces (768 bits)	1142Kp/s	768Mb/s

*Assumes 1MHz in emulation, 64-byte packets, and minimum IPG.

The SpeedBridge virtual environment (shown in Figure 2) combines the flexibility of a virtual environment with the performance and predictability of the High-Density SpeedBridge Adapter for Ethernet. It also calculates accurate latency and rate values for the design under test. The environment requires multiple servers, a PC, and a Palladium Z1 system. The High-Density SpeedBridge Adapter for Ethernet connects the off-the-shelf servers and the Palladium Z1 emulator.

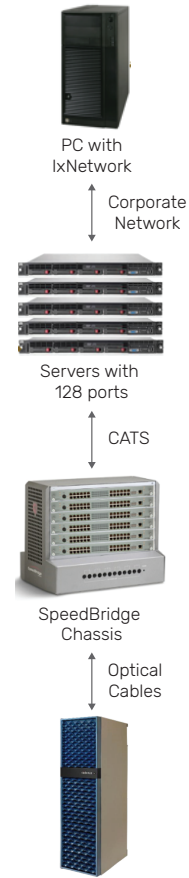


Figure 2: SpeedBridge Virtual Environment

Supported PCS Configurations		
Speed	Interface	Interface Width
1G	TBI	10-bit
10G	XSBI	16-, 20-, 32-, 64-bit
25G	25G-PCS	40-bit
40G	40GBASE-R	16-, 20-, 32-, 40-bit (4 lanes)
50G	50G-PCS	40-bit (2 lane), 80-bit (1-lane)
100G	100GBASE-R	10-, 20-, 32-bit (10 lanes), 40- and 80-bit (4 lanes)
200G	200G-PCS	40-bit (8 lanes), 80-bit (4 lanes)
400G	400G-PCS	80-bit (8 lanes)

The High-Density SpeedBridge Adapter for Ethernet connects to Protium systems using the Protium HDIO board. This High-Density SpeedBridge Adapter for Ethernet uses the Protium high-density target connection to connect to the emulator.

Benefits

Offers rapid verification deployment

- ▶ Provides a pre-validated emulation interface fully compatible with the Palladium Z1 series
- ▶ Provides very high performance during system verification
- ▶ Enables rapid creation of sophisticated system-level environments
- ▶ Compatible with virtual tester
- ▶ Compatible with AVIP

Enables verification IP reuse

- ▶ Works from one project to another project
- ▶ No need for every user to re-invent the solution due to development cost, time, and complexity
- ▶ Improves productivity to get to the first test by simplifying bring-up and reducing test development time by leveraging capabilities of existing testers and system environments

Ensures quality

- ▶ Tested and verified by Cadence and many other user designs
- ▶ Mature product that has been widely deployed in many emulation environments
- ▶ Cadence-provided solution lets you verify your design quickly and efficiently
- ▶ User can develop silicon validation tests in emulation and port them to silicon

Reduces system risk

- ▶ Performs MAC/GMAC testing
- ▶ Runs Ethernet traffic between tester and MAC
- ▶ Protocol and integrity error testing (CRC)
- ▶ Uses loopback tests
- ▶ Faster and more reliable testing with large traffic push through
- ▶ Configures and tests higher-layer traffic using emulation
- ▶ Enables co-verification with software running on the network
- ▶ Runs production system software/drivers to further reduce system risk
- ▶ Validates software stack

Features

Supports multiple ports

Each High-Density SpeedBridge Adapter for Ethernet supports up to twenty-four ports in the following combinations:

- ▶ GMII
- ▶ XGMII (16-bit DDR interface)
- ▶ XGMII (standard 32-bit DDR interface)
- ▶ XLGMII (40G)
- ▶ CGMII (100G)
- ▶ 200G GMII
- ▶ 400G GMII

Multiple adapters can be used to support any number of ports. Scalable architecture supports up to 144 ports per High-Density SpeedBridge Adapter for Ethernet.

- ▶ Remotely configurable
- ▶ Provides a full-speed RJ45 Ethernet interface per port, supporting 1000Base-T Gigabit Ethernet or 100Base-TX Fast Ethernet
- ▶ Integrated cable diagnostics allow reliable connection of multiple SpeedBridge Adapters for HD Ethernet

Conversions for standard interfaces available from MII and GMII modes

- ▶ From MII mode: SMII, S3MII, RMII
- ▶ From GMII mode: RGMII, TBI (alternative of SGMII), and RTBI
- ▶ For XAUI and SGMII converters, please contact your Cadence product marketing representative
- ▶ PMA support for 10G, 25G, 40G, 50G, 100G, 200G, and 400G

Supported emulation speeds

- ▶ Supports full emulation speed range, including ability to start/stop emulated clocks

Enhanced debug capabilities

- ▶ Supports most popular software development environments
- ▶ Supports VCD, FSDB, and SST2 types of waveform through SimVision™ Debug
- ▶ Fully static implementation supports advanced emulation debug features of your Palladium Z1 series systems when debugging your design

Supported ethernet testers

Full-speed front-end can be connected to any third-party tester, PC, or a switch that supports PAUSE frame flow control

Supported packet sizes

- ▶ Handles all regular-size packets and jumbo packets, from 64 bytes up to 16,000 bytes
- ▶ Passes all packets without modification

Enhanced remote configurability and manageability

- ▶ Provides a remote configuration interface for remote user control or scripted, automated operation
 - Compatible with queued testing environments and regression
- ▶ Turns on/off remote configuration interface

Specifications

- ▶ Compatibility
 - IEEE 802.3x standards for Ethernet technology
- ▶ Conversions for standard interfaces
 - MII mode: SMII, S3MII, RMII
 - GMII mode: RGMII, TBI (alternative of SGMII), and RTBI
- ▶ Remote configurability
 - Remote reset and remote configuration using one MII port
- ▶ Third-party testers
 - Supports many third-party testers that support PAUSE frames
- ▶ Flow control
 - Uses PAUSE frame

- ▶ Full-duplex supported
- ▶ Auto-negotiation required
- ▶ SpeedBridge chassis required
- ▶ Packet size
 - From 64 bytes up to 16K bytes
- ▶ Ability to stop the emulator available
- ▶ Configurable inter-frame gap
 - Support both fixed and random gaps
- ▶ Power (typical)
 - 25W maximum per High-Density SpeedBridge Adapter for Ethernet
- ▶ Status indicators
 - LED to indicate power, mode, link, and traffic activity

Cadence Services and Support

- ▶ Cadence application engineers can answer your technical questions by telephone, email, or internet. They can also provide technical assistance and custom training.
- ▶ Cadence-certified instructors teach more than 70 courses and bring their real-world experience into the classroom.
- ▶ More than 25 Internet Learning Series (iLS) online courses allow you the flexibility of training at your own computer via the internet.
- ▶ Cadence Online Support gives you 24x7 online access to a knowledgebase of the latest solutions, technical documentation, software downloads, and more.
- ▶ For more information, please visit www.cadence.com/support for support and www.cadence.com/training for training.

