



Cadence Design Systems, Inc.

Cadence helps speed the design of innovative electronic products that transform the way people live and work. Our System Design Enablement strategy drives our efforts to provide the resources semiconductor and systems companies need to develop highly differentiated products in aerospace, automotive, cloud datacenter, consumer, industrial, internet of things (IoT), mobile, and other market segments. We provide leading electronic design automation (EDA) software and hardware for chip, package, board, and system design as well as semiconductor intellectual property (IP).

Efficient, Fast Chip Design

As the leader in tools designers use to develop custom, analog, RF, and mixed-signal designs, we provide a complete process flow starting with tools that enable fast and accurate entry of design concepts all the way through verifying the designs before they are produced in silicon. By selectively automating non-critical aspects of custom IC design, we allow engineers to focus on precision crafting their designs and the effects on circuit performance.

For designing the digital parts of chips, over the past few years we have introduced an entirely new, next-generation set of tools that improve productivity and speed the design process. This comprehensive tool family speeds the full design cycle from front-end design all the way to final silicon signoff convergence.

The Cadence® IP portfolio includes pre-designed and configurable blocks that can be integrated quickly into chip designs, freeing the design team from re-designing these elements. This portfolio includes Tensilica® DSPs for vision, audio, IoT, and baseband applications; Denali® memory and storage IP; and controllers for the latest standard interfaces including PCI Express®, USB, and Ethernet. We also offer verification IP to verify interface and memory standards.

Corporate Facts

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|------------------------|---|
| Founded | 1988 |
| Corporate Headquarters | 2655 Seely Ave. San Jose, CA 95134 USA |
| Worldwide Locations | www.cadence.com |
| NASDAQ Symbol | CDNS |
| Primary Business | Electronic design automation software, hardware, IP, and services |

To quickly verify chip designs before they are committed to silicon, the Cadence Verification Suite provides four platforms to speed the verification of every step of the design cycle. This unique mixture of software-based solutions with hardware accelerators delivers the shortest turnaround time and predictable quality.

IC Packaging Design

The complexity and performance requirements of today’s semiconductor packages continue to increase while design resources remain static for most organizations—placing a premium on efficiency and productivity. Our packaging and multi-fabric co-design tools deliver the automation and accuracy to expedite the entire design process.

PCB Design

We offer leading PCB layout and routing technologies used by a wide range of designers, from casual hobby designers to companies developing extremely demanding innovative new products. Our tools help designers address everything from simple electrical analysis to multi-board signal simulations in the multi-gigabit range.

System Design

Our system development and functional verification tools help customers quickly get their complete hardware/software systems ready for applications development. We offer software tools as well as hardware-assisted systems to handle the extreme complexity of the system design effort.

Services

Cadence Services collaborates with our customers on methodology and technology adoption, silicon enablement, and talent development to enhance productivity and reduce risk.

A Great Place to Work



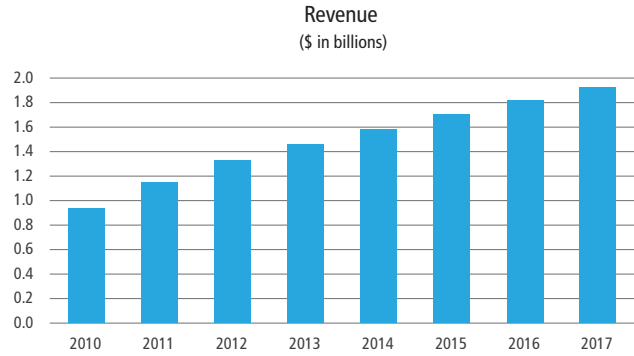
We pride ourselves on creating and sustaining a company culture, “One Cadence—One Team”, that drives market- and industry-leading innovation and business success. Cadence is recognized as a Great Place to Work around the world, including as one of Fortune Magazine’s “100 Best Companies to Work For” over the last three years. We’ve earned additional recognition as a Best Workplace for Asian Americans, Giving Back, Baby Boomers, Recent College Graduates, and Parents. The Great

Place to Work lists are based on employee ratings of different aspects of their workplace culture, including the level of trust they feel toward company leaders, the pride they take in their jobs, and the camaraderie they experience with co-workers.

For More Information

Investor Relations: +1.408.944.7100
Media Relations: +1.408.914.6884

Financial Highlights



Executive Management

Lip-Bu Tan

Chief Executive Officer

Anirudh Devgan, Ph.D.

President

Tom Beckley

Senior Vice President and General Manager, Custom IC & PCB Group

Jim Cowie

Senior Vice President, General Counsel and Secretary

Tina Jones

Senior Vice President, Global Human Resources

Neil Kole

Senior Vice President and Chief Information Officer

Babu Mandava

Senior Vice President and General Manager, IP Group

Nimish Modi

Senior Vice President, Marketing & Business Development

John Wall

Senior Vice President and Chief Financial Officer

Neil Zaman

Senior Vice President, Worldwide Field Operations



Cadence Design Systems, Inc.

Corporate Headquarters: 2655 Seely Avenue, San Jose, CA 95134

P: +1.800.746.6223 (within US) +1.408.943.1234 (outside US) F: +1.408.943.5001

www.cadence.com

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