

Tensilica Academic Program Application

EDUCATIONAL INSTITUTION (“UNIVERSITY”) INFORMATION	
University Name:	Country:
Department:	
Address:	
City:	
State:	Postal Code:
REQUESTING PROFESSOR	
Professor Name:	Phone:
Email:	
SITE/SYSTEM ADMINISTRATOR	
Site Admin Name:	Phone:
Email:	
WEB INFORMATION (to be linked to our University Program web page)	
University URL:	
Department URL:	
Project URL (if any):	
Other Important URL:	
EXECUTIVE SUMMARY	
Provide a brief overview of the project and/or course to be developed and taught utilizing Tensilica tools, including objectives, specific outcome, available recourses, key challenges, measure of effectiveness, etc.	
OUTLINE OF CURRICULUM AND COURSES	
Provide a list of existing engineering curriculum and courses that relate to IC or systems design – use URLs for online information. Include references for supporting papers as appropriate.	

STUDENT/ASSISTANTS OUTREACH

Indicate the number of students/assistants who will use Tensilica tools per year and their graduate level

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PAST ACTIVITY REPORT

If your university is already using Tensilica technology then provide a report of those activities, include supporting URLs and relevant papers from your university. We may link to these from our own website.

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IF RENEWING EXISTING LICENSES

Current expiration date:

Existing Login Name:

AGREED AND ACCEPTED

This Order, including the attached Addendums, together with the terms and conditions at https://www.cadence.com/content/dam/cadence-www/global/en_US/documents/terms-and-conditions/Cadence-TEN-AUA-v1.pdf create a binding agreement between Customer and Cadence. The parties may execute this Order in multiple counterparts, each of which constitutes an original, and all of which, collectively, constitute the only agreement. This Order is effective upon delivery (including electronic delivery) of one executed counterpart from one party to the other party. The terms and conditions of this Order, including pricing are considered Cadence Confidential Information.

Customer Name:

Signature

Printed Name:

Title:

Date:

Tensilica Software

The Tensilica University Program provides leading educational institutions with easy access to Cadence's leading Tensilica® processor configuration and extension tools. This provides the access to the following:

- Software-only Tensilica Xtensa® Processor Generator access
- Vision P6
- ConnX BBE16EP
- ConnX BBE32EP
- ConnX BBE64EP
- HiFi-mini
- HiFi3
- HiFi4
- Fusion F1
- Fusion G3
- Xtensa LX7

- 10 seats for Cadence's Xtensa Software Developers Toolkit (XCC Compiler, Xtensa Instruction Set Simulator (ISS) with Xtensa Modeling Protocol (XTMP), GNU toolchain, Xtensa Xplorer – Standard Edition)
- 10 seats for Xtensa Processor Developers Toolkit (Xtensa Xplorer – Processor Developer's Edition, TIE Compiler, XPRES Compiler)

Special conditions:

- Xtensa FPGA Emulation Kit support available at special conditions
- Xtensa Manufacturable RTL generator available at special conditions

Training:

- Online Tensilica course can be made available