Keynotes
Tuesday, April 02, 10:30am - 12:00pm

Lip-Bu Tan, Chief Executive Office, Cadence

Lip-Bu Tan has served as CEO of Cadence Design Systems, Inc. since January 2009 and has been a member of the Cadence Board of Directors since February 2004. He also serves as chairman of Walden International, a venture capital firm he founded in 1987. Prior to founding Walden, Tan was Vice President at Chappell & Co. and held management positions at EDS Nuclear and ECHO Energy. He is a member of The Business Council and serves on the board of directors of Hewlett Packard Enterprise Co. and Aquantia Corp., as well as the boards of the Electronic System Design Alliance (ESD Alliance) and the Global Semiconductor Association (GSA). He also serves on the Board of Trustees and the School of Engineering Dean’s Council at Carnegie Mellon University (CMU). Tan is the recipient of the 2016 GSA Morris Chang Exemplary Leadership Award. Tan received an M.S. in nuclear engineering from the Massachusetts Institute of Technology, an MBA from the University of San Francisco, and a B.S. from Nanyang University in Singapore.
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Andreas Bechtolsheim, Chief Development Officer, Arista Networks

Andreas Bechtolsheim is a Founder and Chief Development Officer of Arista Networks, a high-speed datacenter switching and cloud networking company. Previously, Andy was a Co-Founder and Chief System Architect at Sun Microsystems, responsible for next generation server, storage, and network architectures. Andy was also a Co-Founder of Granite Systems (acquired by Cisco in 1996) and of DSSD (acquired by EMC in 2014). As a private venture investor, Andy has been involved in the funding of numerous companies including Google, VMware, Mellanox, Brocade, and Magma Design. He has served on the Board of Directors of 25 companies, the majority of which went public or were acquired. Andy earned a M.S. in Computer Engineering from Carnegie Mellon University in 1976. He was a doctoral student in Computer Science and Electrical Engineering at Stanford University from 1977 to 1982. He has been honored with a Fulbright scholarship, a German National Merit Foundation scholarship, the Stanford Entrepreneur Company of the year award, the Smithsonian Leadership Award for Innovation, and he is a member of the National Academy of Engineering.
Anirudh Devgan, PhD, President, Cadence

As President of Cadence, Anirudh Devgan oversees Cadence’s EDA products, including the digital implementation and signoff, functional verification, custom IC design, PCB, and packaging businesses. Additionally, he is responsible for the corporate strategy, marketing, and business development functions. Devgan oversees the Digital & Signoff Group (DSG), the System & Verification Group (SVG), the Custom IC & PCB Group (CPG), and Marketing & Business Development (MBD). Products within DSG include the core digital design, implementation, and silicon signoff business, which encompasses logic synthesis, high-level synthesis, formal verification, test, physical synthesis, place and route, electrical signoff, physical signoff, and design for manufacturing (DFM) technologies. Products within SVG include the Verification Suite, featuring Xcelium™ parallel simulation, hardware system verification with Palladium® and Protium™ platforms, formal and automated verification with JasperGold® Apps, system verification solutions, and Verification IP. Products within CPG include the Virtuoso® design environment, physical design and routing, and simulation product lines for full-custom digital and analog design; infrastructure such as the OpenAccess database and the process design kits (PDKs) that are essential for physical IC design; the Allegro® and OrCAD® design and routing solutions; and Sigrity™ high-speed analysis solutions for PCBs and IC packaging. MBD includes corporate strategy, corporate and strategic marketing, industry standards, and new business development, including M&A. Prior to joining Cadence in 2012, Anirudh Devgan spent seven years at Magma Design Automation as General Manager and Corporate Vice President of Magma’s Custom Design Business Unit, leading the development and introduction of several successful products. Prior to his tenure at Magma, he spent 12 years at IBM in various management and technical positions, where he received numerous awards including the IBM Outstanding Innovation Award. Devgan is an IEEE Fellow and has numerous research papers and patents. Devgan received a Bachelor of Technology degree in electrical engineering from the Indian Institute of Technology, Delhi, and MS and PhD degrees in electrical and computer engineering from Carnegie Mellon University.