

Beijing R&D Vacancies

1. Senior Software Engineer - Characterization RD (Location: BJ)

Position Description:

- The positions are for a developer who will be responsible for designing, implementing, and maintaining library characterization and validation software for use with standard cells, memory and macro blocks, and IO cells.

Requirements:

- The candidates should have two or more years of experiences in developing EDA software.
- Must be proficient in C, C++, TCL, and development in Linux/Unix.
- Knowledge on semiconductor device is strong plus.
- Experience with SPICE or SPICE-like circuit simulation is important.
- Knowledge of Verilog and VHDL is also highly desirable.
- The ideal candidate would have a good understanding of library characterization, IP design, static timing analysis, power analysis, and signal integrity analysis flows.
- Minimum Education Required / Minimum Experience Required : MS, EE, CS, Math or Physics 2
- Preferred Education / Preferred Experience: Ph.D. , EE, CS, Math or Physics 3-5

2. Lead PV Engineer- Characterization (Location: BJ) ×2 Vacancies

Position Description:

- The positions are for a PV who will be responsible for designing, implementing, and maintaining library characterization and validation software for use with standard cells, memory and macro blocks, and IO cells.

Requirements:

- The candidates should have two or more years of experiences in developing EDA software.
- Must be proficient in C, C++, TCL, and development in Linux/Unix.
- Knowledge on semiconductor device is strong plus.
- Experience with SPICE or SPICE-like circuit simulation is important.
- Knowledge of Verilog and VHDL is also highly desirable.
- The ideal candidate would have a good understanding of library characterization, IP design, static timing analysis, power analysis, and signal integrity analysis flows.
- Minimum Education Required / Minimum Experience Required : MS, EE, CS, Math or Physics 2
- Preferred Education / Preferred Experience: Ph.D. , EE, CS, Math or Physics 3-5

3. Software Engineer for Fastspice Simulator (Location: BJ)

Position Description

- Maintain and develop Cadence fastspice simulator.
- Be responsible for writing specifications, designing and implementing product enhancements and fixes.
- Needs to work with other global R&D teams..

Position Requirements

- Education requirement: Master with above 2 years work experience or Ph.D. in EE/CS/related research area.
- Must be skilled in C/C++ programming, familiar with development under Linux/Unix environment.
- Strong background in numerical computation and programming.
- Knowledge of Analog Mixed-signal design and semiconductor device is a strong plus.
- Good English communication skills both verbally and in writing.

4. Software Engineer for Virtuoso ADE GXL (Location: BJ)

Position Description:

- Dealing with manufacturing variation is becoming an increasingly important aspect of the custom digital and analog circuit design process. Virtuoso ADE GXL is a software tool for designers that offers statistical analysis, yield improvement, and design optimization capabilities to address this growing problem. We are seeing a talented software developer with a strong background in statistical analysis, data mining and modeling.

Position Requirements:

- Strong background in statistical analysis, data mining, and modeling
- Demonstrated proficiency in C++ and general software development skills
- Master in Computer Science or Electrical Engineering required, PhD preferred.
- Master with 5 years working experience or PhD with 2-3 working experience.
- Excellent communication skill in both mandarin and English.
- Working knowledge of Matlab a plus.
- Experience with Cadence Virtuoso or analog circuit design a plus.

5. Software Configuration Management (CM) Engineer (Location: BJ)

Job Description:

- This position involves managing daily software build, test, and release processes, and configuration management support for other cross functional teams. The responsibility also includes maintaining and enhancing the existing automation systems, designing and developing new automation systems, software integration, and project management.

Position Requirements

- Computer Science or Engineering Bachelor degree.
- Good programming background.
- Experience of Unix/Linux system.
- Good problem solving & analysis skills.
- Good interpersonal, verbal, and written communication skills.
- Fast and self learner.

6. Senior Software Engineer for Verilog-A simulator development(Location: BJ)

Position Description:

- Develop, enhance and maintain Verilog-A simulator.

Requirements:

- Familiar with Spice, Verilog-A, Verilog-AMS language
- Skilled in C++ programming, familiar with development under Linux/Unix environment.
- Analog circuit or digital simulator development experiences.
- Well understanding on circuit simulation technology, including MNA, dc, tran method.
- Good mathematic background & knowledge.
- Be familiar with Analog Mixed-signal design is a plus
- EE or CS Master degree with at least 2 years related working experience or above

7. Software Engineer (Location: BJ)**Job Description:**

- Develop, enhance and maintain mixed signal circuit simulator which support Verilog, Verilog-A, Verilog-AMS, VHDL, VHDL-AMS and SystemVerilog.

Requirements:

- Familiar with Spice, Verilog, Verilog-A, Verilog-AMS, VHDL-AMS language
- Skilled in C++ programming, familiar with development under Linux/Unix environment.
- Analog circuit or digital simulator development experiences
- Be familiar with Analog Mixed-signal design is a plus
- EE or CS Master degree with at least 2 years related working experience or above

8. Senior Software Engineer (Location: BJ)**Job Description:**

- Develop, enhance and maintain mixed signal circuit simulator which support Verilog-A, Verilog-AMS, VHDL-AMS in spice netlist, with some direction from manager or senior engineers

Job Description:

- Familiar with Spice, Verilog, Verilog-A, Verilog-AMS, VHDL-AMS language - Analog circuit or digital simulator development experiences
- Skilled in C++ programming, familiar with development under Linux/Unix environment.
- Be familiar with Analog Mixed-signal design is a plus - EE or CS Master degree with at least 2 years EDA related working experience or above

Intern**1. Software Configuration Management Intern (Location: BJ)****Job Description:**

- This position involves managing daily software build, test, and release processes, and configuration management support for other cross functional teams.

Job Requirements:

- MS candidate in Computer Science, Engineering, or related area.
- Good programming background.
- Experience of Unix/Linux system.

- Fast and self learner

2. PV Intern for Circuit Simulation (Location: BJ)

Job Description:

- Work together with a group of professionals on a variety of PV/QA projects to enhance circuit simulation product quality.

Job Requirements:

- Students who are pursuing master's degree in Microelectronics or EE.
- Customer IC design experience.
- UNIX/Linux experience.