VIRTUOSO: CUSTOM IC DESIGN COURSES

Silicon that’s right, on time. The Virtuoso® platform enables the world’s fastest, most silicon-accurate analog, custom digital, RF, and mixed-signal design.

Virtuoso: Analog/Mixed-Signal Courses

- Analog Modeling & Simulation with Spice
- Virtuoso Design Entry and Basic Simulation
- Virtuoso NeoCircuit
  - Circuit Sizing & Optimization
- Virtuoso Aptilia
  - Specification Driven Environment
- Virtuoso Analog Simulation Techniques
- Virtuoso Spectre Circuit Simulator
- Virtuoso Spectre RF Simulation
- Virtuoso UltraSim
  - Full-Chip Simulator
- Virtuoso Analog Modeling with Verilog-A
- Virtuoso AMS Designer

Assura: Design for Manufacturing Courses

- Assura Verification
- Assura Parasitic Extraction (RCX)
- Assura Rules Writer
  - (DKULS Rules)
- Assura RF
- Assura RCX Developer

Electrical Verification

- Fire & Ice QXC
  - Gate-Level Extraction
- VoltageStorm
  - PE Power Analysis
- Gate-Level Power Grid Analysis Using VoltageStorm
- Transistor-Level Power Grid Signoff Using VoltageStorm

Infrastructure Courses

- SKILL Language Programming
  - for IC Layout Design
- SKILL Programming for CAD
- SKILL Development of Parameterized Cells
- SKILL Programming for CAD

Virtuoso: Physical Design & Skill Courses

- Virtuoso Layout Design Basics
- Virtuoso Layout Optimizer
- Chip-Level Routing with Chip Assembly Router
- Virtuoso Connectivity-
  - Driven Layout
- Module Generators
  - EE
- Virtuoso Chip Assembly Router
- Space-based Router
- Digital and Analog Placers
- Floorplanning your Design
- Yield Optimize
- Chip Optimizer

SKILL Language Programming
- ILS
- ILS
- ILS
- ILS

iLS: Internet Learning Series, self-paced EDA classes delivered over the web. This class could also be delivered Live.

EE: Engineer Explorer, designed for more experienced users of Cadence tools.